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PTO/SB/21 (08-00)

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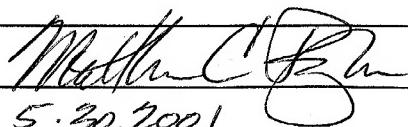
Total Number of Pages in This Submission

Application Number	09/751,526
Filing Date	December 29, 2000
First Named Inventor	Nandu J. Marketkar, et al.
Group Art Unit	2631
Examiner Name	Not yet assigned
Total Number of Pages in This Submission	Attorney Docket Number 42390P10081

## ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment / Reply	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 5.30.2001
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<input type="checkbox"/> Response to Missing Parts/ Incomplete Application	<input type="checkbox"/> Remarks	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

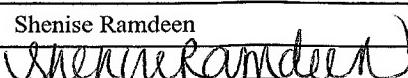
## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Matthew C. Fagan, Reg. No. 37,542
Signature	
Date	5.30.2001

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Substitute for form 1449A/PTO

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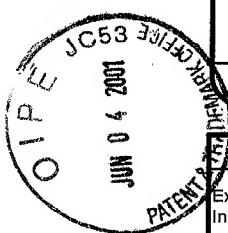
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**Complete if Known**

Application Number	09/751,526
Filing Date	12-29-00
First Named Inventor	Marketkar, et al.
Group Art Unit	2631
Examiner Name	Not yet assigned

Attorney Docket Number 42390P10081



## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	09/751,526
Sheet	2	of	2	Filing Date	12-29-00
				First Named Inventor	Marketkar, et al.
				Group Art Unit	2631
				Examiner Name	Not yet assigned
				Attorney Docket Number	42390P10081

<b>OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS</b>		
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Farjad-Rad, Ramin, et al., "A 0.3-μm CMOS 8-Gb/s 4-PAM Serial Link Transceiver", IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, pp. 757-764 (May 2000).
		Osaka, Hideki, High Performance Memory Interface for DDR-SDRAM II: XTL (Crosstalk Transfer Logic), Hitachi Ltd., Systems Development Laboratory, pp. 1-20 (September 15, 2000).
		Stearns, Thomas H., Flexible Printed Circuitry, McGraw-Hill Companies, Inc., pp. 109-110 and 232-235 (1996).
		XTL Evaluation System, Hitachi Ltd., Systems Development Laboratory, pp. 1-14 (September 15, 2000).
		Yang, Chih-Kong Ken, et al., "A 0.5-μm CMOS 4.0-Gbit/s Serial Link Transceiver with Data Recovery Using Oversampling", IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, pp. 713-722 (May 1998).
		U.S. Patent Application No. 09/713,702, filed November 15, 2000, entitled INTERCONNECT MECHANICS FOR ELECTROMAGNETIC COUPLER, by Nandu J. Marketkar, Thomas F. Knight, Jr., John R. Benham, Mark E. Naylor and John L. Critchlow.
		U.S. Patent Application No. 09/751,442, filed December 29, 2000, entitled CLAMP TO SECURE CARRIER TO DEVICE FOR ELECTROMAGNETIC COUPLER, by Nandu J. Marketkar, Mark E. Naylor and John L. Critchlow.
		U.S. Patent Application No. 09/751,527, filed December 29, 2000, entitled ELECTROMAGNETIC COUPLER SOCKET, by Nandu J. Marketkar, Thomas F. Knight, Jr., Mark E. Naylor and John L. Critchlow.

Examiner Signature	Date Considered
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